

IN THE SPECIFICATION:

Please amend the last paragraph of page 10 (continuing to page 11) of the specification as follows:

Fig. 1 is a schematic view showing wiring of the liquid crystal display device as viewed from an image observation window side. A first substrate SUB1 includes a plurality of gate lines GL which extend in the X direction (horizontal direction) and are arranged in parallel in the Y direction (vertical direction) in the pixel region AR, a plurality of drain lines DL which extend in the Y direction and are arranged in parallel in the X direction in the pixel region AR ~~except at an edge of the pixel region AR~~, and a plurality of holding capacitance lines (also referred to as storage lines) CL which are arranged parallel to the gate lines GL. A region in which the gate lines GL and the drain lines DL which cross each other are formed constitutes a pixel region AR. In the pixel region AR, a region which is surrounded by two neighboring gate lines GL and two neighboring drain lines DL form one pixel. It is possible to display a color image on a panel front surface using three types of pixels (red pixel, green pixel, blue pixel). Here, gate lines GL are driven by a gate driver GDr and the drain lines DL are driven by a drain driver DDr.